

ANTONIOS T. REIZIS

CURRICULUM VITAE



I. PERSONAL DETAILS

Full Name : ANTONIOS T. REIZIS
Title : M.Eng. Electrical Engineer, G.W.C.¹, T.C.G.²
(Democritus University of Thrace, Certif. Numb. : 1016 / 11-07-1995)
• Field : Electronic Engineer and Informatics' Systems Technology's Engineer, Master in Electrical Engineering (M.Eng.)
• Direction : Telecommunications' Engineer and Space Sciences' Engineer,
• Dissertation / Thesis Field : Space Technology – Nuclear Spectroscopy of Charged Particles – Data Acquisition Systems,
• Research Activity Field : Aerospace Technology – Defense Systems – Avionics – A.S.I.C. – M.E.M.S. – V.L.S.I. – Telecommunications
Licensee Industrial, Energy's, Manufacture and Mechanical Engineer
Scientific Consultant – Inspector – Expert – Specialist
Professor in Telecommunications, Computer – Informatics' Systems Technology, Electronic and Mechanical Engineering

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City of Alexander The Great – Giannitsa
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GREECE – EU

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URL / WWW : <http://raytonics.cjb.net>
Date of Birth : May 29, 1971
Sex : Male
Citizenchip / Nationality : Greek – EU
Marital Status : Single
Military Service Status : Completed

II. EDUCATION AND RESEARCH ACTIVITIES

Independent Scientific Consultant in Matters of Research : LOGOS – Logos Research – Defense Systems Research and Development Department, GREECE – EU 02-14-2000 up to Present

Research Field : Defense / Arming [Recognizing(Reconnaissance) / Air(Dog) – Fighting] Systems

Title of Subject : UAVs (Unmanned Air Vehicles)

Project : Research, Development and Application of Full – Custom Made Mixed – Hybrid (Analog – Digital) Rad – Hard (Radiation Hardened) C.M.O.S. (Complementary Metal Oxide Silicon) (+3.3V) V.L.S.I. (Very Large Scale of Integration) A.S.I.C. (Application Specific Integrated Circuits) and M.E.M.S. (MicroElectroMechanical Systems) Fully Compensated Very Low Power Dissipation, Defense Subsystems (GPS – Global Positioning Systems, Gyros, CCD – Charge Coupled Devices and IIR – Image InfraRed Systems) applied to Arming Systems (Micro UAVs and Small UAVs for the U.S. Navy) using “ State of the Art ” Military Technology.

Independent Scientific Consultant in Matters of Research : University of Nottingham, School of Chemical, Environmental and Mining Engineering, U.K. – EU 09-01-1996 up to 09-26-2000

Project : Research, Development and Application of Full – Custom Made Specific Application Software and Electronic Circuits, applied to Novel Scientific Experiments of Applied Electrochemistry, using Advanced Technology.

Special Graduate Student : Part – time Graduate Program in Electrical Engineering (Telecommunications and Space Sciences), The Johns Hopkins University (J.H.U.), Applied Physics Laboratory (A.P.L.), U.S.A. 02-07-1996

¹ Government Works Contractor

² Member of Technical Chamber of Greece – EU, T.C.G. / T.E.E.

Research Assistant : *The Johns Hopkins University, Applied Physics Lab., U.S.A. 01-06-1996 up to 04-08-1997*

Project : Research, Development and Application of Full – Custom Made Mixed Rad – Hard C.M.O.S. (+3.3V) V.L.S.I. A.S.I.C. and M.E.M.S. Fully Compensated Very Low Power Dissipation, applied to Novel Scientific Experiments and Subsystems of Satellites / Spacecrafts / Arming Systems (“ Low Cost Planetary Missions ” – The Smaller, Faster, Better, Cheaper Approach), using “ State of the Art ” Aerospace – Military Technology (National Aeronautics and Space Administration – N.A.S.A., Jet Propulsion Laboratory – J.P.L., U.S. Air Force, Army, Navy, Defense Advanced Research Projects Agency – D.A.R.P.A., Department Of Defense – DoD and the Departments of Transportation, Energy, and Treasury) Project within the framework of the inter – university’s collaboration, between the Space Department (S.D.) of The Johns Hopkins Univ. / Applied Physics Lab., U.S.A. and the D.U.Th., GREECE – EU

M.Eng. in Electrical Engineering : Democritus University of Thrace, GREECE – EU **11-07-1995**
Grade : 8.00 (in the 0 – 10, “ Very Good ”)

Final Year Project : “ Pulse Height Analysis System Design (P.H.A.) ”. R & D referred to Bouquet experiment of Relict – 2, Space Mission within the framework of the International Solar – Terrestrial Physics program (I.S.T.P.) D.U.Th. – Xanthi, GREECE – EU **10-25-1995**
Grade : 10.0 (in the 0 – 10, “ Excellent ”)

III. ACADEMIC ACTIVITIES

- Author of 3 Papers, within the framework of University’s Courses (within the framework of Greek – EU and International Programs)
- Author of 2 Papers, included in University’s Writings (within the framework of Greek – EU and International Programs)
- Author of 1 Dissertation / Thesis (within the framework of an International Space Program)
- Speaker of 2 University’s Presentations in Greece – EU and U.S.A. (within the framework of International Space Programs)
- Co – Author of 1 Conference Paper in U.S.A. (within the framework of an International Space Program)
- Co – Author of 1 Journal Paper in Germany – E.U.

IV. MISCELLANEOUS

Languages : English (Very Good), French, Greek (Native)

Computer Languages include : FORTRAN, BASIC, Turbo PASCAL, Turbo C++, ASSEMBLY (Atmel’s microcontrollers, National Semiconductor’s COPs family, Motorola 68XXX family and Zilog Z – 80), VHDL, LADDER, HTML, DHTML, J++, JAVAscript, RDBMS

Computer Applications include : Windows .NET, XP, 2000, CE, ME, 9X, 3X and NT 4.0, MS-DOS, Macintosh OS, VAX / VMS, Alpha / Open VMS, DECnet, UNIX X11, X – Windows, MS – Kermit, Linux, Novel, TCP / IP, ATM, FTP, Telnet, Gopher, E – Commerce, Netscape Communicator 7, Microsoft Internet Explorer 6, FrontPage 2002, Microsoft NetMeeting 3.01, Microsoft Outlook (Express) 2002 (6.0), Lotus’ Software, Microsoft Project 2002, Primavera’s Software, MatLAB 6.5, LView, GSView, Xview, MS – OfficeXP, CorelDRAW 11, Corel PhotoPaint 11, Adobe PhotoShop 7, Adobe Illustrator 10, Adobe PageMaker 7, Adobe Premiere 6.5, QuarkXPress 6, Macromedia’s Software, AutoDesk’s AutoCAD R2004, CADdy++ Electrical, 4M’s Software, P – CAD 2002, smARTWORK v1.4r7, Hiwire II 2.3, Eagle v4.09r2, Protel’s Software, OrCAD 9.2, Nano – PLC PL707, Intellectron’s Hydra, CNC & SCADA Applications, EPROM Programmer, Intel PLD Programmer, Cadence SOLO, Magic VLSI Layout Editor, L – Edit Layout Editor 2, McCAD, BSIM, IRSIM, RSIM, Electronics Workbench 5.12 (Multisim, Ultiboard, Ultiroute and Commsim) 2001 SP2, Microcap 7, AIM – Spice, P – Spice 9.2, H – Spice v2001.4, IS – Spice, T – Spice v8, METAWAVES, National Instruments’ LabVIEW 6.1, P.A.M.E.L.A., EG&G ORTEC’s Maestro, Philips’ I2C Monitoring Software e.t.c.

V. AUTHORIZATIONS – CERTIFICATES – DIPLOMAS – LICENSES – PARTICIPATIONS

- 9 Greek – EU, European and International Licenses / Diplomas
- 6 Greek – EU and International Special Personal Authorizations in writing [Administration of Secret and Top – Secret (CONFIDENTIAL / SECRET / TOP SECRET / RESTRICTED), Graded and Non – Graded Mater (NATO SECRET – SECURITY CLEARANCE)]
- 20 Greek – EU and International Certificates
- Membership in 30 Greek – EU and International Committees, Organizations and Universities

VI. AWARDS – DISTINCTIONS – GRANTS – HIGH DISTINCTIONS – PRAISES – TROPHIES

- 6 High Distinctions during the time of High School and Senior High School Studies
- 6 Awards / Trophies during the time of Elementary School, High School and Senior High School Studies
- 21 Distinctions during the time of Elementary School, High School, Senior High School, University Studies and Military Service
- 8 Praises during the time of Elementary School, High School, University Studies and Military Service
- 2 Grants during the time of Graduate Studies from Greece – EU and U.S.A. (within the framework of an International Space Program)

VII. WORK EXPERIENCE

- 36 Participations in Greek – EU and International Programs (Projects) and previous Work Positions

VIII. CONFERENCES, MEETINGS AND SEMINARS ATTENDED

- 6 Conferences in Greece – EU and U.S.A.
- Over 500 Seminars in Greece – EU, U.S.A. and Internet
- 6 Meetings in Greece – EU and U.S.A.

IX. INTERESTS

- Aerospace Technology and Applied Physics, Novel New Generation Space and Satellite Subsystems, Avionics, (Defense) Arming systems [Air(Dog) – Fighting, Recognizing(Reconnaissance), Submarines e.t.c.], Satellite Telecommunications, Telecommunication Systems (Bluetooth, G.P.S., IR.D.A., U.S.B.)
- Microelectronics, Nanotechnology, Design of V.L.S.I. A.S.I.C., High frequencies, Microwave, Microelectronics, MicroElectroMechanics Systems (M.E.M.S.), solid state components and circuits
- Research and Development of hardware systems associated with the fields of Power Electronics, Optoelectronics, Automatic Control Systems, Nuclear Physics Electronics, Instrumentation and Measurement Systems
- Computer Aided Design – Research – Development and Manufacturing Systems (C.A.D. – C.A.M. – C.A.E. – C.I.M. Systems)
- Commercial, Industrial, Medical, Security / Fire – Detection, Military and Aerospace Electronics, Optoelectronics, Radioelectronics, Microelectronics – Microelectromechanics, Microwave Systems

X. ELECTROMECHANICAL PROJECT COMPETENCES

- Research, Development and Application of Basic and Full – Custom Made Systems
- Certificates – Computations – Designs – Good Performance Guarantees – Investigations – Licenses – Preliminary and Finally Reports and Itemized Surveys – Proposals
- Evaluations – Expert's / Specialist's Reports – Inspections – Supervisions
- Cost and Planning Analysis – Economic and Technical Estimates – Optimizations – Simulations
- Constructions – Installations – Maintenance – Performance's Quality Checks – Placing – Service – Support – Tests
- Contract Works of (Micro)Electronics / Mechanics – Electromechanics – Telecommunication's Installations – Projects – Systems – Clusters

XI. RECOMMENDATIONS – REFEREES

- Available upon request.

XII. APPENDIX

XII – III. ACADEMIC ACTIVITIES

- Author of a Paper entitled “ L.U.T. – Look Up Table – V.L.S.I. C.M.O.S. A.S.I.C. Chip Design, using P.L.A.s – Programmable Logic Arrays ” within the framework of the obligatory Course of the 8th Semester entitled “ V.L.S.I. I ” of Prof. F. Tsalidis (within the framework of an European Program)
D.U.Th. – Xanthi – Greece – EU May 1993 (The Authors / Members of the Group : V. Katos, A. Reizis, G. Sioutis)
 - Author of a Paper entitled “ Special Instrumentation Amplifier Design ” within the framework of the optional Course of the 8th Semester entitled “ Automation’s Electronic Measurements ” of Assist. Prof. Y. Andreadis
D.U.Th. – Xanthi – Greece – EU June 1993
 - Author of a Paper entitled “ Research, Development and Application of a Car Beeper ” included in the University’s Writing entitled “ Introduction to Discrete Mathematics for Computer Science ” of Assoc. Prof. D. A. Georgios
D.U.Th. – Xanthi – Greece – EU July 1994
 - Author of a Paper entitled “ Active Hybrid Fully – Compensated Excitation and Operation Control System of Synchronous Electric Power Generators connected to a Power Grid Design ” within the framework of the “ Dissertation / Thesis of N. P. Athanasiadis, D.U.Th. – Xanthi – Greece – EU 1995 ” (within the framework of a Greek – British Program) D.U.Th. – Xanthi – Greece – EU Mars 1995
 - Author – Speaker of the Dissertation / Thesis entitled “ Pulse Height Analysis System Design ” (referred to Bouquet experiment of Relict – 2, Space Mission within the framework of the International Solar – Terrestrial Physics program – (I.S.T.P.))
D.U.Th. – Xanthi – Greece – EU October 25, 1995
 - Author – Speaker of a Graduate Paper entitled : “ Research and Development of a Fully – Compensated Custom Very Low Power CMOS (+3.3V) V.L.S.I. A.S.I.C. Rad – Hard 3 – wire’s High – Speed I2C – Serial Interface (Design, Simulation, Testing and Documentation) – Preliminary Design Review ” (within the framework of an International Space Program) within the framework of the Graduate Course entitled “ VLSI Technology and Application II ” (525.429) of Instructor Martin Fraeman
The Johns Hopkins University – Applied Physics Laboratory – Laurel MD, U.S.A. February 15, 1996
 - Co – Author of a Journal Paper with title “ Microprocessor Control Techniques and Modelling of Adjustable Speed Drives ”, ELEKTRIE, Berlin 56 (2002) 05-08, pp. 180-184, Germany – EU (inEnglish)
(The Authors: Dr Nikolaos Athanasiadis, Mr Antonios Reizis)
- e.t.c.

XII – V. AUTHORIZATIONS – CERTIFICATES – DIPLOMAS – LICENSES – PARTICIPATIONS

- Diploma (M.Eng.) of the Department of Electrical and Computers Engineering of the Polytechnic School of Xanthi of D.U.Th. – Greece – EU
- B' Category's Greek / European Driving License
- License to Practise Profession of Electrical Engineer (Greece – EU)
- General License of Electrical Installation Execution All of Specialties' and Classes' (Greece – EU)
- General License Elaboration of Reports, the Supervision of Execution and the Supervision of Operation of Mechanical Installations (Greece – EU)
- Diploma A' Class of the Register of Experience of Constructors (R.E.C. / M.E.K.) in Electromechanical, Hydraulic Under Pressure and Industrial – Energy’s Works (Greece – EU)
- Diploma A' Class of the Register of Contract Works Corporations (R.C.W.C. / M.E.EP.) in Electromechanical and Industrial – Energy’s Works (Greece – EU)
- Certificate of Instruction in Lighting Protection Systems, Impulse Overvoltages Protection and Groundings (E.L.E.M.K.O. – Greece – EU)
- Research Member of “ Bouquet ” – Scientific Experiment Group of “ Relict – 2 ” – Space Mission of the “ International Solar – Terrestrial Physics program ” (I.S.T.P.)
- Research Member of Space Sciences Research Instrumentation – 3 Group of J.H.U. / A.P.L. / S.D.
- Special Graduate Member of the Part – time Graduate Program in Electrical Engineering, The Johns Hopkins University (J.H.U.)

- Independent Consulting Research Member of School of Chemical, Environmental and Mining Engineering, University of Nottingham
 - Member of Technical Chamber of Greece – EU (T.C.G. / T.E.E.)
 - Member of the Pension Fund of Engineers and Government Works Contractors (P.F.E.G.W.C. / T.S.M.E.D.E. – Greece – EU)
 - Membership in Internal Revenue Service of Giannitsa – Pella – Greece – EU (I.R.S. / D.O.Y.)
 - Member of the Town Planning Office of Giannitsa – Pella – Greece – EU
 - Member of the Panel of Inspectors, Experts – Specialists of the District Attorney's Office of the Court Of First Instance of Giannitsa – Pella – Greece – EU from the 1998
 - Member of the Register of Experience of Constructors (R.E.C. / M.E.K.) of the Department of Environment, Country Planning and Government Works (D.E.C.P.G.W. / Y.PE.HO.D.E. – Greece – EU)
 - Member of the Register of Contract Works Corporations (R.C.W.C. / M.E.EP.) of the Department of Environment, Country Planning and Government Works (D.E.C.P.G.W. / Y.PE.HO.D.E. – Greece – EU)
 - Member of the Soma of Permanent Committee, Special Scientific Committee (Register of Scientific Responsibilities) and Committee of Study of Informatics, Telecommunication and New Technologies of T.C.G. – Greece – EU
 - Member of the Soma of Permanent Committee, Special Scientific Committee (Register of Scientific Responsibilities) and Committee of Study of Research – Technology of T.C.G. – Greece – EU
 - Member of the Soma of Permanent Committee, Special Scientific Committee (Register of Scientific Responsibilities) and Committee of Study of Education of T.C.G. – Greece – EU
 - Member of the Soma of Permanent Committee, Special Scientific Committee (Register of Scientific Responsibilities) and Committee of Study of Engineers – Government Officers of T.C.G. – Greece – EU
- e.t.c.

XII – VI. AWARDS – DISTINCTIONS – GRANTS – HIGH DISTINCTIONS – PRAISES – TROPHIES

- Distinction of a Paper entitled “ Research, Development and Application of a Car Beeper ” included in the University's Writing entitled “ Introduction to Discrete Mathematics for Computer Science ” of Assoc. Prof. D. A. Georgios
D.U.Th. – Xanthi – Greece – EU July 1994
(20% of the Copyright of this University's Writing for every Academic Session from 1994 up to present)
 - Grant – “ U.S.D. 46,000 ” – for research activity (within the framework of an International Space Program) from 10-01-1995 up to 12-31-1997 (D.U.Th.)
 - Grant – “ U.S.D. 8,000 ” – for research activity (within the framework of an International Space Program) from 10-01-1995 up to 12-31-1997 (J.H.U.)
- e.t.c.

XII – VII. WORK EXPERIENCE

- Work Experience of School's Physics and Chemistry Laboratory Responsible
Elementary School of Palefito D.C. – City of Alexander The Great – Giannitsa – Greece – EU Academic Sessions 1981-1983
High School of Kariotissa D.C. – City of Alexander The Great – Giannitsa – Greece – EU Academic Sessions 1984-1986
- Work Experience in Laboratory of Physics, Inorganic and Organic Chemistry and Electrochemistry (preparation up to 1,000 chemical matters – combinations – preparations)
Self – Employment: Palefito D.C. – City of Alexander The Great – Giannitsa – Greece – EU 1981-1990
- Work Experience of Research, Development and Application (of Basic and Full – Custom Made Systems), Preliminary and Finally Report and Itemized Survey – Design – Simulation – Optimization – Cost and Planning Analysis – Economic and Technical Estimate – Construction – Installation – Maintenance – Performance's Quality Check – Placing – Service – Support – Tests of *Electronic – Electromechanic – Telecommunication Installations – Devices – Systems (execution up to 2,000 acts)*
Self – Employment / Personal Corporation : Palefito D.C. – City of Alexander The Great – Giannitsa – Greece – EU
1981 up to Present

- Work Experience of Research, Development and Application (of Basic and Full – Custom Made Systems), Preliminary and Finally Report and Itemized Survey – Design – Simulation – Optimization – Cost and Planning Analysis – Economic and Technical Estimate – Construction – Installation – Maintenance – Performance's Quality Check – Placing – Service – Support – Tests of *Security's, Fire detection's and Fire – Extinguishing's Automate Control Systems*
Collaboration : XANA Security S.A., Giannitsa – Greece – EU 1989-1993
- Work Experience of Research, Development and Application (of Basic and Full – Custom Made Systems), Preliminary and Finally Report and Itemized Survey – Design – Simulation – Optimization – Cost and Planning Analysis – Economic and Technical Estimate – Construction – Installation – Maintenance – Performance's Quality Check – Placing – Service – Support – Tests of *Industrial Electronic Automate Control Systems – Programmable Logic Controllers (P.L.C.s)*
Employer : Prodomos Pavlidis S.A. – Skidra – Edessa – Greece – EU 1993
- Research – Development – Design – Simulation – Optimization – Realization of a “ L.U.T. – V.L.S.I. C.M.O.S. A.S.I.C. Chip Design, using P.L.A.s ” within the framework of the obligatory Course of the 8th Semester entitled “ V.L.S.I. I ” of Prof. F. Tsalidis (within the framework of an European Program)
D.U.Th. – Xanthi – Greece – EU May 1993 (The Partners : V. Katos, A. Reizis, G. Sioutis)
- Research – Development – Design – Simulation – Optimization – Realization – Performance's Quality Check of a “ Special Instrumentation Amplifier Design ” within the framework of the optional Course of the 8th Semester entitled “ Automation's Electronic Measurements ” of Assist. Prof. Y. Andreadis
D.U.Th. – Xanthi – Greece – EU June 1993
- Research – Development – Design – Simulation – Optimization – Realization – Performance's Quality Check of a “ Car Beeper ” included in the University's Writing entitled “ Introduction to Discrete Mathematics for Computer Science ” of Assoc. Prof. D. A. Georgios
D.U.Th. – Xanthi – Greece – EU July 1994
- Research – Development – Design – Simulation – Optimization – Realization – Performance's Quality Check of the Engineering Model of a “ Pulse Height Analysis System Design ” (referred to Bouquet experiment of Relict – 2, Space Mission within the framework of the International Solar – Terrestrial Physics program – (I.S.T.P.)
D.U.Th. – Xanthi – Greece – EU September 1993 up to October 1995
- Research – Development – Design – Simulation – Optimization – Realization – Performance's Quality Check of a “ Active Hybrid Fully – Compensated Excitation and Operation Control System of Synchronous Electric Power Generators connected to a Power Grid Design ” within the framework of the “ Dissertation / Thesis of N. P. Athanasiadis, D.U.Th. – Xanthi – Greece – EU 1995 ” (within the framework of a Greek – British Program)
D.U.Th. – Xanthi – Greece – EU Mars 1995
- Research Activity (within the framework of an International Space Program) in the Field “ V.L.S.I. – Space Technology ” as a member of Space Sciences Research Instrumentation – 3 Group (SRI – 3, SRM – former S1A) of the S.D. / A.P.L. / J.H.U.
Employer : A.P.L. / J.H.U. , U.S.A. 01-06-1996 up to 04-08-1997
- Research, Development and Application of Full – Custom Software and Specific Application Electronic Circuits, applied to Novel Scientific Experiments of Electrochemistry, using Advanced Technology.
Independent Scientific Consultant in Matters of Research :
University of Nottingham, School of Chemical, Environmental and Mining Engineering, U.K. 09-01-1996 up to 09-26-2000
- Administrative and Technical Support of Establishment, Development and Running of an Advertising Center of Artwork's Processing and Printing and Multimedia
Fany Grafity – Palefito D.C. – City of Alexander The Great – Giannitsa – Greece – EU 09-29-1997 up to Present
- Battalion's Electrical Engineer** : 523 I.B. – Mavrodendri / Kozani / Greece – EU 03-23-1998 up to 05-13-1998
- 4th Office's Clerk* : 501 M.I.B. – Pentaplatanos / Giannitsa / Greece – EU 05-13-1998 up to 07-11-1998
- Battalion's Electrical Engineer ** : 501 M.I.B. – Pentaplatanos / Giannitsa / Greece – EU 05-13-1998 up to 07-11-1998
- 2nd Office's Clerk* : 607 I.B. – Rodopolis / Serres / Greece – EU 07-11-1998 up to 08-14-1998
- Battalion's Electrical Engineer ** : 607 I.B. – Rodopolis / Serres / Greece – EU 07-11-1998 up to 08-14-1998

*Office's Clerk : Special Personal Authorization in writing of Hellenic Army Staff (H.A.S. / Y.E.S.) [Administration of Secret and Top – Secret (CONFIDENTIAL / SECRET / TOP SECRET / RESTRICTED), Graded and Non – Graded Mater (NATO SECRET – SECURITY CLEARANCE)]. Work Experience of Office Set – Up and Computerization, Proposal, Report and Itemized Surveys Execution, Cost and Planning Analysis, Economic and Technical Estimates, Performance's Quality and Quantitative Checks e.t.c. Computerization and Administration of Battalion's 4^{ou} Office Supply Depot's.

**Battalion's Electrical Engineer : Care for the right and the smoothly operation of the power grid and its connected devices. Inspection, design and supervision of right execution of additional labor if needed [Central heating, Perimetric lighting and Camp's Security system]. Expert's reports in existing electrical sub – networks.

***Company's Platoon Sergeant (IF) : Work Experience of Administration and Management of Personnel – Company of 60 Solder – division and programming of labor, duties and day – offs [planning], Company Computerization, care for the right operation and good looking of Company.

- 4th Office's Clerk* : 501 M.I.B. – Pentaplatanos / Giannitsa / Greece – EU 08-14-1998 up to 01-18-1999
 - Battalion's Electrical Engineer ** : 501 M.I.B. – Pentaplatanos / Giannitsa / Greece – EU 08-14-1998 up to 01-18-1999
 - 4th Office's Clerk* : 514 S.C.B. – Protokklisi / Soufli / Greece – EU 01-21-1999 up to 09-23-1999
 - Platoon Sergeant (IF) of the Commandment's Company***: 514 S.C.B. – Protokklisi / Soufli / Greece – EU 04-13-1999 up to 09-23-1999
 - Battalion's Electrical Engineer ** : 514 S.C.B. – Protokklisi / Soufli / Greece – EU 08-30-1999 up to 09-23-1999
 - Research, Development and Application of Full – Custom Made Mixed Rad – Hard C.M.O.S. (+3.3V) V.L.S.I. A.S.I.C. and M.E.M.S. Fully Compensated Very Low Power Dissipation, Defense Subsystems (GPS, Gyros, CCD and IIR) applied to Arming Systems (Micro UAVs and Small UAVs for the U.S. Navy) using “ State of the Art ” Military Technology. Independent Scientific Consultant in Matters of Research : LOGOS – Logos Research – Defense Systems Research and Development Department 02-14-2000 up to Present
- e.t.c.

XII – VIII. CONFERENCES, MEETINGS AND SEMINARS ATTENDED

- “ Space Physics and Technology ” Conference
(within the framework of an International Space Program) D.U.Th. – Xanthi – Greece – EU April 1993
- “ Space Physics and Technology ” Seminar Series
D.U.Th. – Xanthi – Greece – EU November 1993 up to February 1994
- “ Cassini Space Mission / MIMI Experiment ” Meeting (preliminary design)
(within the framework of an International Space Program) D.U.Th. – Xanthi – Greece – EU June 1994
- “ Mars '96 Space Mission / SLED – 2 Experiment ” Meeting (flight model design)
(within the framework of an International Space Program) D.U.Th. – Xanthi – Greece – EU June 1995
- “ Telemedicine ” Conference
The Johns Hopkins University – Applied Physics Laboratory – Laurel MD U.S.A. February 1996
- “ Superconductors ” Conference
The Johns Hopkins University – Applied Physics Laboratory – Laurel MD U.S.A. February 1996
- “ NEAR Space Mission Launch ” Meeting The Johns Hopkins University – Applied Physics Laboratory – Laurel MD U.S.A.
(within the framework of an International Space Program) February 1996
- “ Second IAA International Conference on Low – Cost Planetary Missions ” The Johns Hopkins University – Applied Physics Laboratory – Laurel MD U.S.A. (within the framework of an International Space Program) April 16-19, 1996
- “ Very Low Power Integration Circuits Technology ” Seminar
The Johns Hopkins University – Applied Physics Laboratory – Laurel MD U.S.A. May 1996
- “ Time – Of – Flight Chip (T.O.F.) Preliminary Design ” Meeting The Johns Hopkins University – Applied Physics Laboratory – Laurel MD U.S.A. (within the framework of an International Space Program) May 1996
- “ National Instruments' LabVIEW ” Seminar
The Johns Hopkins University – Applied Physics Laboratory – Laurel MD U.S.A. Mars 06, 1997
- “ Interball I & II I.S.T.P.'s Space Mission ” Meeting The Johns Hopkins University – Applied Physics Laboratory – Laurel MD U.S.A. (within the framework of an International Space Program) Mars 1997
- “ Time – Of – Flight Chip (T.O.F.) Testing ” Meeting The Johns Hopkins University – Applied Physics Laboratory – Laurel MD U.S.A. (within the framework of an International Space Program) April 03, 1997
- “ Fujitsu College of Imaging ” Net – Seminar
Fujitsu Computer Products of America, Inc. – <http://www.netseminar.com> April 05, 1999
- “ Changing the Game in HDL Design with Integrated Capture, Design Management and Simulation ” Net – Seminar
Mentor Graphics Corporation – <http://www.netseminar.com> April 13, 1999
- “ Maximizing Clock Tree Performance For Deep Sub – Micron Designs ” Net – Seminar
Frequency Technology, Inc. – <http://www.netseminar.com> May 04, 1999
- “ Our Innovation, Your Advantage. Design Direct™ PLD implementation software gives designers a valuable head start. ” Net – Seminar
Vantis Corporation – <http://www.netseminar.com> May 26, 1999

- “ Putting Analog On The Bus ” *Net – Seminar*
Xicor, Inc. – <http://www.netseminar.com> July 21, 1999
- “ High – Speed PCB Design ” *Net – Seminar*
Demystified, Cadence Design Systems, Inc., PCB Systems Division – <http://www.netseminar.com> March 16, 2000
- “ Selection Criteria for Real Time Clocks ” *Net – Seminar*
Xicor, Inc. – <http://www.netseminar.com> March 23, 2000
- “ Upgrading Hardware and Software Over the Internet ” *Net – Seminar*
Xilinx, Inc. and GoAhead Software, Inc. – <http://www.netseminar.com> April 26, 2000
- “ Web – Facilitated PCB Design and Design Transfer ” *Net – Seminar*
Agilent Technologies, Inc. – <http://www.netseminar.com> April 27, 2000
- “ Noise Figure Measurements with a Spectrum Analyzer ” *Net – Seminar*
Tektronix, Inc. – <http://www.netseminar.com> May 04, 2000
- “ DDR SDRAM Industry Summit Technology Rollout “Mission Accomplished” ” *Net – Seminar*
DDR SDRAM Industry – <http://www.netseminar.com> May 18, 2000
- “ The Joule of Power Electronics: Ultracapacitors in the Design of Power Systems, PowerCache Ultracapacitors ” *Net – Seminar*
Maxwell Technologies, Inc. – <http://www.netseminar.com> June 29, 2000
- “ Golden Innovations in LDMOS for Avionics ” *Net – Seminar*
Philips Semiconductors and Avnet, Inc. – <http://www.netseminar.com> July 18, 2000
- “ Analog & Mixed Signal Online Conference ” *Net – Seminar*
CMP Media, Inc. – <http://www.netseminar.com> September 14, 2000
- “ Gigabit Over Copper ” *Net – Seminar*
A Network Computing Event, sponsored by Cisco Systems – <http://www.netseminar.com> February 08, 2001
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